



Asteralabs Expands Interoperability Leadership to Propel Next-Gen PCIe 6.x Ecosystem

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Comprehensive Cloud-Scale Interop Lab testing of Scorpio Smart Fabric Switches advances PCIe 6.x ecosystem enablement, fast-tracking customer platform designs, development, and time-to-market

SANTA CLARA, Calif., March 13, 2025 (GLOBE NEWSWIRE) -- Astera Labs, Inc. (Nasdaq: ALAB), a global leader in semiconductor-based connectivity solutions for AI and cloud infrastructure, today announced the expansion of its Cloud-Scale Interop Lab to accelerate time-to-market of PCIe® 6.x AI platforms powered by Scorpio Smart Fabric Switches. Rigorous interoperability and performance testing of Scorpio P-Series Fabric Switches with a wide-range of PCIe 6.x exercisers, analyzers, GPUs, CPUs, NICs, SSDs, and Switches across multiple PCIe generations and topologies enables customers to design with confidence, minimize interoperation risk, and reduce overall development time and costs.

Chris Petersen, Fellow, Technology and Ecosystems, Astera Labs, said, “Beyond base level interoperability, Astera Labs’ Cloud-Scale Interop Lab embodies our cloud-native philosophy — leveraging our early ecosystem engagement, system validation boards, advanced diagnostics, and performance and system testing with real-world applications. This expansion of our interoperability leadership empowers customers to optimize performance, increase reliability, and accelerate deployment of their next-generation PCIe 6.x systems.”

Higher bandwidth PCIe 6.x technology is essential to maximize GPU, CPU, and AI accelerator performance for AI workloads in hyperscale systems, but its adoption introduces new connectivity challenges with increased speed, mixed generation complexity, and scale. To address these challenges, Scorpio P-Series Fabric Switches and Aries 6 PCIe/CXL® Smart DSP Retimers undergo rigorous PCIe link, RAS, and system testing in complex PCIe topologies over varying channel insertion loss budgets. Unlike interop programs that are one-dimensional, Scorpio and Aries are tested running real-world applications on production-ready systems with leading root complexes, endpoints, and operating systems.

This development extends Astera Labs’ leadership in system interoperability and builds upon recent ecosystem milestones, including the company’s [first public demonstration of end-to-end PCIe 6.x interoperability](#) between Scorpio P-Series Fabric Switches and Micron’s PCIe 6.x SSD using GPU-Direct Storage at DesignCon 2025.

Customers can request custom interop reports featuring Scorpio Smart Fabric Switches and a broad range of PCIe 6.x/PCIe 5.x devices at www.asteralabs.com/getreports.

Industry Support

Raghu Nambiar, Corporate Vice President, Data Center Ecosystems and Solutions, AMD said, “AMD and Astera Labs are committed to PCIe technologies to enable next-generation cloud and AI infrastructure solutions. Astera Labs plays a critical role in ensuring robust interoperability with next-generation AMD processors, driving platform developments to ensure performance for emerging workloads, including AI.”

Dr. Joachim Peerlings, Vice President of Network and Data Center Solutions at Keysight Technologies, said, “Astera Labs’ fabric switch solution with support for PCIe 6.x technology is an important milestone for the ecosystem. Keysight looks forward to collaborating with Astera Labs testing efforts in their Cloud-Scale Interop Lab to enable compliant, interoperable PCIe 6.x-based platforms that advance next generation AI and cloud infrastructure.”

Alvaro Toledo, VP and GM of Micron Data Center Storage, said, “Micron is excited to advance the PCIe Gen6 ecosystem through our collaboration with Astera Labs. Our joint demonstration of end-to-end interoperability at the highest performance levels is a significant milestone. As the industry leader in developing PCIe Gen6 SSDs, Micron is pushing technological boundaries and setting the stage for this technology to become the storage standard for future data centers.”

Leno Park, Vice President of Nand Product Planning, Samsung Electronics, said, “Our collaboration with Astera Labs ensures that Samsung’s cutting-edge PCIe storage solutions are rigorously validated in the Cloud-Scale Interop Lab, helping customers achieve seamless interoperability and maximize performance of their advanced PCIe-based platforms. This cooperation is vital to enabling the emerging PCIe ecosystem, delivering faster, more reliable storage solutions that power next-generation data centers for AI applications.”

Joe Mendolia, Vice President of Marketing, Protocol Solutions Group, Teledyne Lecroy, said, “As a leader in PCI Express 6.x protocol and physical layer testing with our innovative analysis tools, we value our close partnerships with other ecosystem leaders who are at the forefront of bleeding edge interconnect developments. Teledyne Lecroy welcomes the opportunity to partner with Astera Labs in the Cloud-Scale Interop Lab, helping to ensure PCIe 6.x platforms achieve seamless interoperability through our integrated and automated compliance testing system. We’re excited that our collaboration with companies such as

Astera Labs can help drive and ensure the future of advanced technologies such as AI."

Robert Lin, President, Enterprise & Networking Business Group, Wistron, said, "As AI workloads drive demand for higher performance and scalability in hyperscale data centers, robust connectivity becomes paramount. Our close collaboration with Astera Labs enables us to accelerate time-to-market of our latest AI platforms through comprehensive design validation and pre-production testing."

Resources

- Webpage: [Cloud-Scale Interop Lab](#)
- Video: [How We Test – Scorpio](#)
- Collateral: [Request Interop Report](#)

About Astera Labs

Astera Labs is a global leader in purpose-built connectivity solutions that unlock the full potential of AI and cloud infrastructure. Our Intelligent Connectivity Platform integrates PCIe®, CXL®, and Ethernet semiconductor-based solutions and the COSMOS software suite of system management and optimization tools to deliver a software-defined architecture that is both scalable and customizable. Inspired by trusted relationships with hyperscalers and the data center ecosystem, we are an innovation leader delivering products that are flexible and interoperable. Discover how we are transforming modern data-driven applications at www.asteralabs.com.

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